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Viva Question For Hdl Lab

VHDL Programming Lab viva questions and answers 1. VHDL means. Very high speed integrated circuit hardware description language. 2. List the types of HDL? VHDL and Verilog HDL . 3. Software used in VHDL lab is? Xilinx 4. Name any two programming tools of VHDL? FPGA, CPLD and ASIC 5. FPGA means. Field programmable gate array. 6. CPLD means.

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These questions are very useful as FPGA viva questions also. Question -1: Write a simple VHDL program for D Flipflop and D latch. Answer -1: Refer D Flipflop VHDL Code and D Latch VHDL Code Question -2: Write a VHDL program for 4X1 MUX(Multiplexer). Answer -2: Refer 4X1 MUX VHDL Program . Question -3: Write a VHDL code for 8 to 3 encoder.

10 VHDL,Verilog,FPGA interview questions and answers

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Question 1. What Is Vhdl? Answer : VHDL stands for "VHSIC Hardware Description Language." VHSIC, in turn, stands for "Very High Speed Integrated Circuit," which was a U.S. Department of Defense program. Question 2. What Can Be The Various Uses Of Vhdl? Answer : The VHDL language can be used for several goals like - To synthesize digital circuits.

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The test for high-density lipoprotein cholesterol (HDL-C) is used as part of a lipid panel to screen for unhealthy levels of lipids and to determine your risk of developing heart disease and help make decisions about what treatment to use if you have borderline risk, intermediate risk, or high risk. The other components of a lipid panel typically include total cholesterol, LDL cholesterol (LDL ...

HDL Cholesterol - Lab Tests Online

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