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As discussed in the above question, the V_t depends on the voltage connected to the Body terminal. It also depends on the temperature, the magnitude of V_t decreases by about 2mV for every 1°C rise in temperature. 10. Give the Cross-sectional diagram of the CMOS. VLSI VIVA Questions and Answers :

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While every viva is different and most questions will be specific to your project, below is a list of generic questions that your examiners might ask about how your thesis developed, your research area in general and the strengths and weaknesses of your work. Some find that preparing a few answers is a great way to build confidence beforehand and is often a good way to settle into the viva, as examiners sometimes start by asking basic, broad questions.

40 practice questions for viva preparation - NDPH Student Blog

Home/ lab viva questions/LICA part 1 Viva Questions on Op Amps: 1.What are the advantages of integrated circuits? · increased package density. · Cost reduction . · Increased system reliability. · Improved functional performance. · Increased operating speeds. · Reduction in power consumption.

Linear Integrated circuits lab Viva Questions Part 1 ~ ECE ...

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EDC Lab Viva Questions and Answers Electrical Lab Edit Question No. 01: What is modulation? Answer: Modulation is the process of varying some characteristic of a periodic wave with an external signal. Radio communication superimposes this information bearing signal onto a carrier signal. These high frequency carrier signals can be transmitted ...

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Home Electrical Lab Electrical Circuits Lab Viva Questions and Answers Electrical Circuits Lab Viva Questions and Answers Electrical Lab Edit Question ... VL always remains constant. Non-Ideal or Practical Voltage Source For a practical source, the terminal voltage falls off with an increase in load current.

Electrical Circuits Lab Viva Questions and Answers ...

Viva Questions 1. Define AM and draw its spectrum? 2. Draw the phasor representation of an amplitude modulated wave? 3. Give the significance of modulation index? 4. What are the different degrees of modulation? 5. What are the limitations of square law modulator? 6. Compare linear and nonlinear modulators? 7. Compare base modulation and emitter modulation? 8.

Analog Communications Lab Manual (S/W)

7 Responses to Interview & Viva questions and answers on Digital Electronics. Barun Singh says: at . Nice collection of questions on Digital electronics.but this can be more elaborated by adding questions on Multiplexer , adder , subtracter and muffy loop.With an addition of objective question on digital electronics can make this article more ...

Interview and viva questions and answers on Digital ...

Computer Networks VIVA Questions :-1. Explain What is Network? A network is a set of devices connected by physical media links. A network is recursively is a connection of two or more nodes by a physical link or two or more networks connected by one or more nodes.

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Sub: 8086 VIVA QUESTIONS Compiled by: L. Krishnananda, Assistant Professor, REVA Institute of Technology, Bangalore 1 1. Give examples for 8 / 16 / 32 bit Microprocessor? - 8-bit Processor - 8085 / Z80 / 6800; 16-bit Processor - 8086 / 68000 / Z8000; 32-bit Processor - 80386 / 80486.

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What are super, primary, candidate and foreign keys? Ans: A super key is a set of attributes of a relation schema upon which all attributes of the schema are functionally dependent. No two rows can have the same value of super key attributes. A Candidate key is minimal superkey, i.e., no proper subset of Candidate key attributes can be a superkey. A Primary Key is one of the candidate keys.

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